AN INTEGRATED CIRCUIT ARRANGEMENT FOR MULTIPLE-SENSOR TYPES WITH SELECTABLE FRONT ENDS ABSTRACT OF THE DISCLOSURE

A low power programmable digital filter integrated circuit architecture has a programmable front end servicing up to 4 digital sources. Changing the programming of the front end permits digital outputs from different sensor types to be accommodated. Digital filtering is accomplished using a digital signal processor that is programmable to accommodate different decimation ratios.

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